28/1

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.:

09/903,059

Confirmation No.:

9375

First Named Inventor:

Constantin Bulucea

Filing Date:

July 10, 2001

Group Art Unit:

2814

Examiner:

Farahani, D.

Atty. Docket No.:

NS-4971 US

Title:

Gate-Enhanced Junction Varactor

Assignee:

National Semiconductor Corporation

# 11 Jan Jung

San Jose, California 27 January 2003

Attn: Official Draftsperson

COMMISSIONER FOR PATENTS

Washington, D.C. 20231

## **SUBMISSION OF FORMAL DRAWINGS**

Sir:

Applicant submits nineteen (19) sheets of formal drawings, consisting of Figures 1, 2a, 2b, 3, 4, 5, 6, 7, 8a, 8b, 9a, 9b, 10a, 10b, 11a, 11b, 12, 13, 14, 15, 16, 17, 18, 19a, 19b, 20a, 20b, 21, 22, 23a, 23b, 24, 25, 26 and 27 in the above-named application.

The enclosed drawings incorporate the changes requested in the Amendments submitted submitted 13 June 2002 and 8 January 2003 for amending the drawings of the present application. The Office Action mailed 28 August 2002 specifies that the drawings filed 13 June 2002 are accepted. Inasmuch as the only drawings submitted 13 June 2002 were the red-marked drawings which accompanied the 13 June 2002 Amendment to the drawings and which illustrated the drawing changes requested in that Amendment, the statement in the 28 August 2002 Office Action indicating acceptance of the drawings filed 13 June 2002 presumably means that the proposed drawing corrections submitted 13 June 2002 are accepted.

LAW OFFICES OF SKJERVEN MORRILL LLP San Jose, CA San Francisco, CA

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In any event, if the drawing changes requested in the 13 June 2002 and 8 January 2003 are acceptable, please substitute the enclosed formal drawings for the informal drawings now on file.

If there are any questions regarding these drawings, please call the undersigned at (408) 453-9200, ext. 1371.

EXPRESS MAIL LABEL NO:

EV 240 085 512 US

Respectfully submitted,

Ronald J. Meetin

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